

# Model Name: GA-B85M-D3H

www.xinxunwei.com 400-800-9990

SHEET TITLE Revision 1.11

01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	CPU_LGA1150-A
05	CPU_LGA1150-B
06	CPU_LGA1150-C
07	DDR III CHANNEL A 1,2
08	DDR III CHANNEL B 1,2
09	PCH_FDI,DMI,USB,PCIE,NVRAM
10	PCH_DP,CLK BUFFER
11	PCH_HOST,SATA,PCI
12	PCH_GPIO,CTRL,AUDIO
13	PCH_PWR,GND
14	PCI EXPRESS*16 SLOT
15	PCI EXPRESS*4 SLOT
16	PCI SLOT1,2
17	ITE 8728 LPC IO
18	COM,KB_MS_USB,USB30_20
19	HWM,FAN CTRL,OV,-PROCHOT
20	DUAL BIOS
21	FP,FUSB,SPK,SATALED
22	Realtek ALC892-GR
23	REAR AUDIO JACK
24	REALTEK RTL8111F
25	DISCRETE POWER
26	ATX , CLOCK GEN, TPM
27	VCORE ISL95820_1

SHEET TITLE

28	VCORE ISL95820_2
29	RT8120_DDR POWER
30	LPT, M3 POWER
31	DVI, HDMI
32	IT8892E

Gigabyte Technology		
Title		
Cover Sheet		
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BLOCK DIAGRAM

PCI EXPRESS X16

DVI, HDMI

RGB

PCI EXPRESS X4

PCI BRIDGE ITE IT8892

Realtek RTL8111F

USB2.0 PORTS X12

USB3.0 PORTS X4

INTEL LGA1150

VRD12.5

PCH (B85)

Realtek ALC892

AUDIO PORTS : FRONT AUDIO  
LIN\_ OUT LINE\_IN MIC CD\_IN  
SURROUND CEN/LEF SURR BACK

CHANNEL A  
DDRIII DIMM X 2

CHANNEL B  
DDRIII DIMM X 2

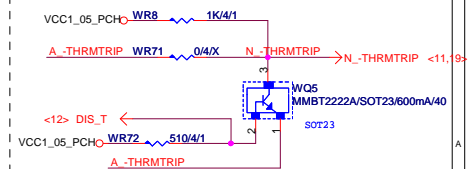
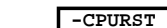
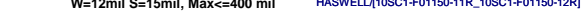
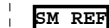
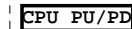
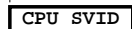
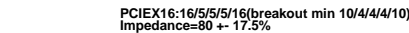
SATAIII\*4/SATAII\*2

DUAL BIOS

LPC I/O ITE8728

I/O PORTS :  
COM KB/MS

FRONT PANEL / FAN



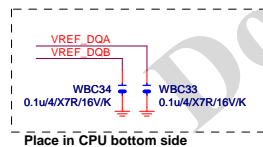
## LGA1150 (A)

LGA1150A	
MAAA0 AU13	DDR0_MA0
MAAA1 AV16	DDR0_MA1
MAAA2 AU16	DDR0_MA2
MAAA3 AW17	DDR0_MA3
MAAA4 AW17	DDR0_MA4
MAAA5 AW18	DDR0_MA5
MAAA6 AV17	DDR0_MA6
MAAA7 AT18	DDR0_MA7
MAAA8 AU18	DDR0_MA8
MAAA9 AT19	DDR0_MA9
MAAA10 AW11	DDR0_MA10
MAAA11 AV19	DDR0_MA11
MAAA12 AU19	DDR0_MA12
MAAA13 AY10	DDR0_MA13
MAAA14 AT20	DDR0_MA14
MAAA15 AU21	DDR0_MA15
MODT_A0 AW10	DDR0_ODT0
MODT_A1 AY8	DDR0_ODT1
MODT_A2 AW9	DDR0_ODT2
MODT_A3 AU8	DDR0_ODT3
AW33	DDR0_ECC0
AW33	DDR0_ECC1
AU31	DDR0_ECC2
AU31	DDR0_ECC3
AU33	DDR0_ECC4
AU33	DDR0_ECC5
AT31	DDR0_ECC6
AW31	DDR0_ECC7
SBA00 SBA01	DDR0_BA0
SBA01 SBA02	DDR0_BA1
SBA02 SBA02	DDR0_BA2
CKEA0 CKEA1	DDR0_CKE0
CKEA1 CKEA2	DDR0_CKE1
CKEA2 CKEA3	DDR0_CKE2
CKEA3 CKEA3	DDR0_CKE3
CSA0 CSA1	DDR0_CS_N0
CSA1 CSA2	DDR0_CS_N1
CSA2 CSA3	DDR0_CS_N2
CSA3 CSA3	DDR0_CS_N3
DCLKA0 DCLKA0	DDR0_CLK_P0
DCLKA0 DCLKA1	DDR0_CLK_N0
DCLKA1 DCLKA1	DDR0_CLK_P1
DCLKA1 DCLKA2	DDR0_CLK_N1
DCLKA2 DCLKA2	DDR0_CLK_P2
DCLKA2 DCLKA3	DDR0_CLK_N2
DCLKA3 DCLKA3	DDR0_CLK_P3
DCLKA3 DCLKA3	DDR0_CLK_N3
AW12	RSVD
RSVD	
SRASA SRASA	DDR0_RAS*
SWEA SWEA	DDR0_WE*
AW20	RSVD
AW27C	RSVD
SCASA SCASA	DDR0_CAS*
WR61 D4/SH1/MX	DDR_RESET
WC4 0.1u4/X7R/16V/KX	

HASWELL[10SC1-F01150-11R\_10SC1-F01150-12R]

## LGA1150 (B)

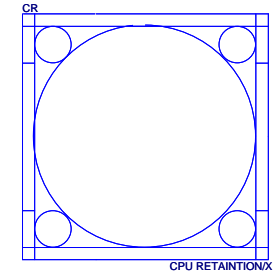
LGA1150B	
MAAB0 AL19	DDR1_MA0
MAAB1 AK23	DDR1_MA1
MAAB2 AM22	DDR1_MA2
MAAB3 AM23	DDR1_MA3
MAAB4 AP23	DDR1_MA4
MAAB5 AY24	DDR1_MA5
MAAB6 AY24	DDR1_MA6
MAAB7 AV25	DDR1_MA7
MAAB8 AU26	DDR1_MA8
MAAB9 AV25	DDR1_MA9
MAAB10 AP18	DDR1_MA10
MAAB11 AY25	DDR1_MA11
MAAB12 AV26	DDR1_MA12
MAAB13 AR15	DDR1_MA13
MAAB14 AV27	DDR1_MA14
MAAB15 AY28	DDR1_MA15
MODT_B0 AM17	DDR1_ODT0
MODT_B1 AL16	DDR1_ODT1
MODT_B2 AM16	DDR1_ODT2
MODT_B3 AK15	DDR1_ODT3
AM26	DDR1_ECC0
AM25	DDR1_ECC1
AP25	DDR1_ECC2
AP26	DDR1_ECC3
AL26	DDR1_ECC4
AL25	DDR1_ECC5
AR26	DDR1_ECC6
AR25	DDR1_ECC7
SBA00 SBA01	DDR1_BA0
SBA01 SBA02	DDR1_BA1
SBA02 SBA02	DDR1_BA2
CKEB0 CKEB1	DDR1_CKE0
CKEB1 CKEB2	DDR1_CKE1
CKEB2 CKEB3	DDR1_CKE2
CKEB3 CKEB3	DDR1_CKE3
CSB0 CSB1	DDR1_CS_N0
CSB1 CSB2	DDR1_CS_N1
CSB2 CSB3	DDR1_CS_N2
CSB3 CSB3	DDR1_CS_N3
DCLKB0 DCLKB0	DDR1_CLK_P0
DCLKB0 DCLKB1	DDR1_CLK_N0
DCLKB1 DCLKB1	DDR1_CLK_P1
DCLKB1 DCLKB1	DDR1_CLK_N1
DCLKB2 DCLKB2	DDR1_CLK_P2
DCLKB2 DCLKB3	DDR1_CLK_N2
DCLKB3 DCLKB3	DDR1_CLK_P3
DCLKB3 DCLKB3	DDR1_CLK_N3
SCASB SCASB	DDR1_CAS*
SRASB SRASB	RSVD
SWEB SWEB	DDR1_RAS*
SWEB SWEB	DDR1_WE*
VREF_DQA VREF_DQB	DDR_VREF_DQ0
VREF_DQA VREF_DQB	DDR_VREF_DQ1



Place in CPU bottom side

HASWELL[10SC1-F01150-11R\_10SC1-F01150-12R]

## LGA1150 (CR)



LGA1150\_P



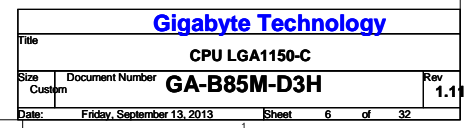
ILM\_BP/1156/CSP/ILM\_BP/1156/CSP/[12KRC-0F0001-52R\_12KRC-0F0001-51R]

## DDR BUS

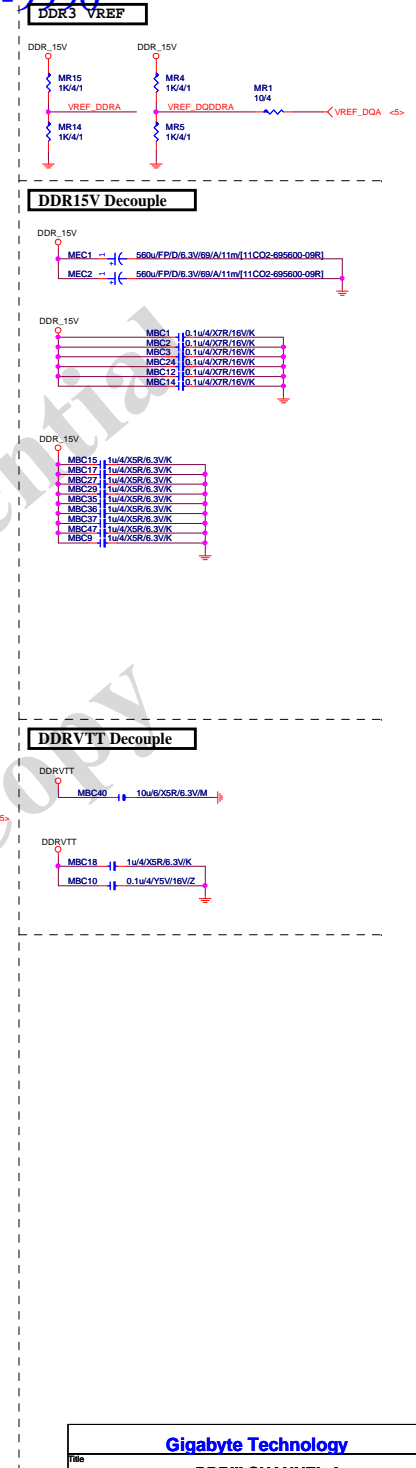
<7> MODT_A[0..3]	MODT_A[0..3]
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<7> MDA[0..63]	MDA[0..63]
<8> MDB[0..63]	MDB[0..63]
<7> DQSA[0..7]	DQSA[0..7]
<7> -DQSA[0..7]	-DQSA[0..7]
<7> MAA[0..15]	MAA[0..15]
<8> MAAB[0..15]	MAAB[0..15]
<8> DQSB[0..7]	DQSB[0..7]
<8> -DQSB[0..7]	-DQSB[0..7]

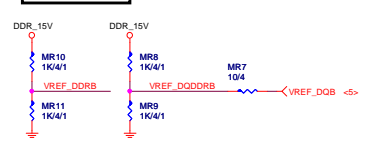
Gigabyte Technology

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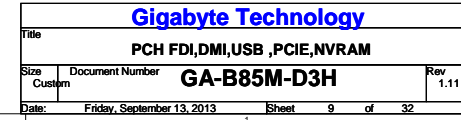


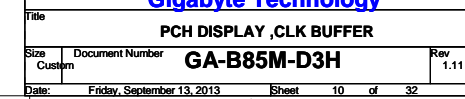
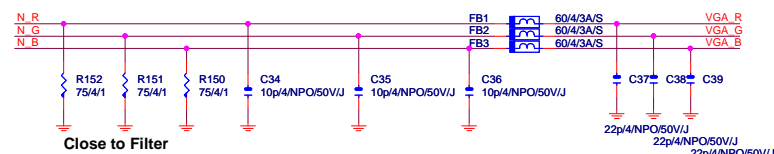
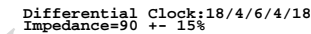




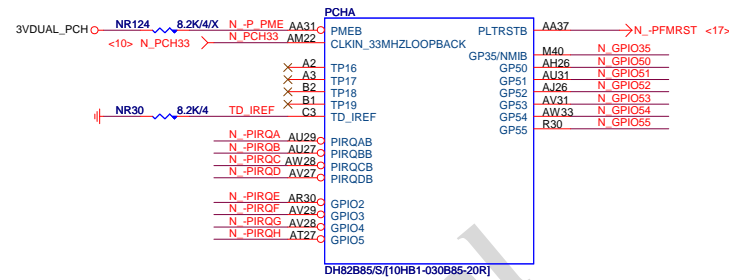




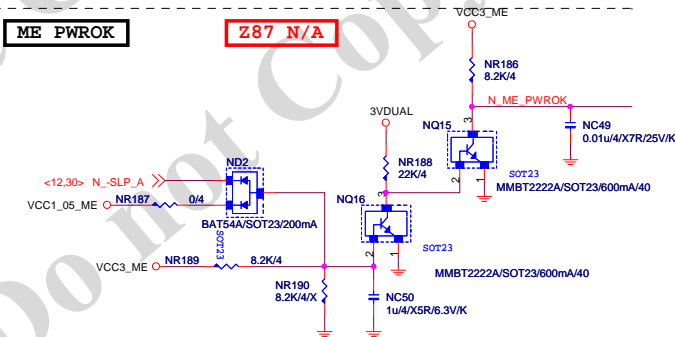




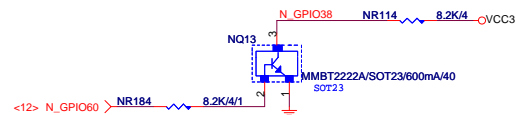
**PCH** (A)



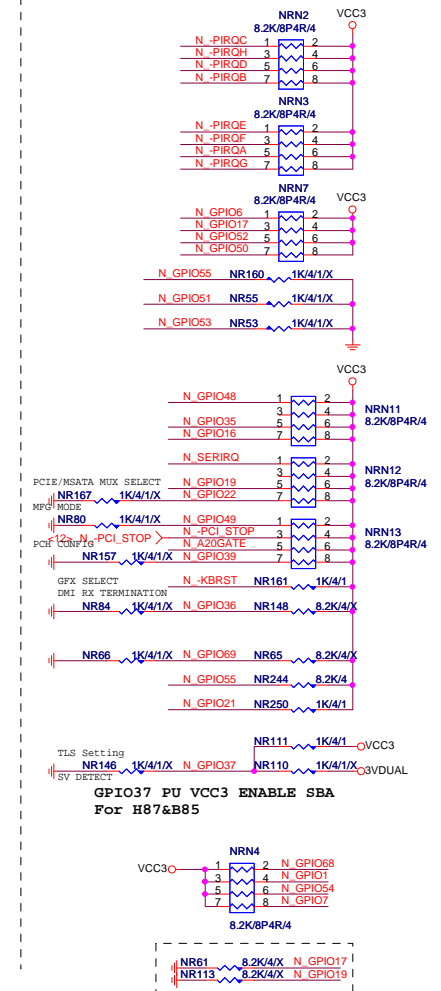
## Z87 N/A

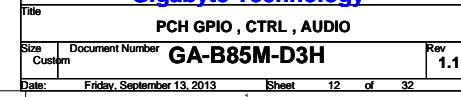
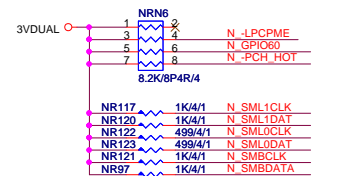


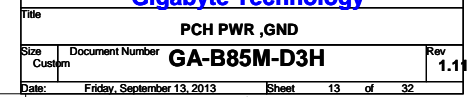
GPIO38 Ctrl



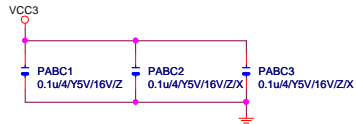
PCH	PU/PD
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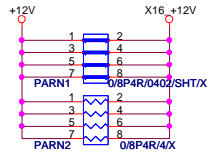




# PCIEX16 CAP



# PCIEX16 PROTECT SHT

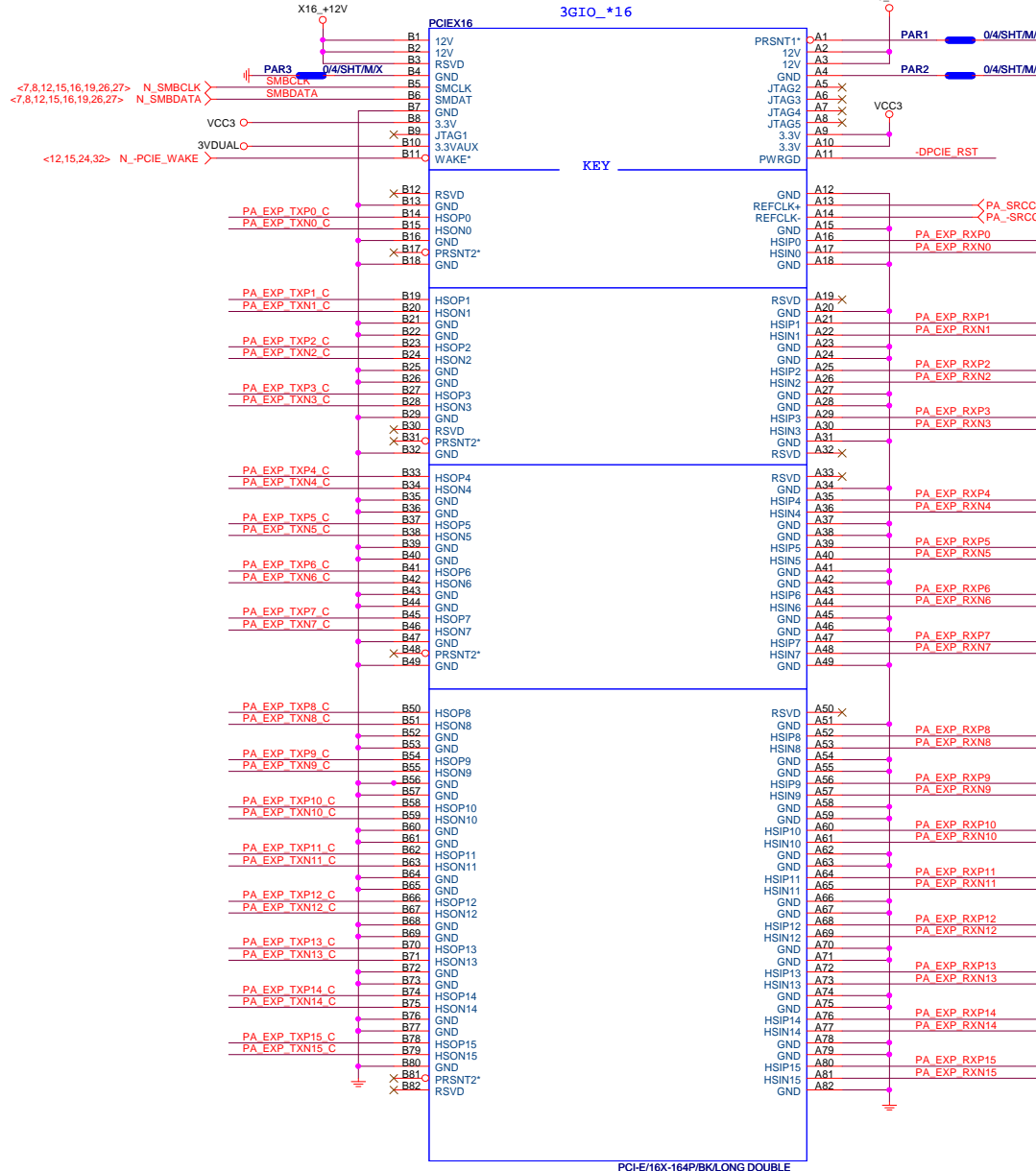


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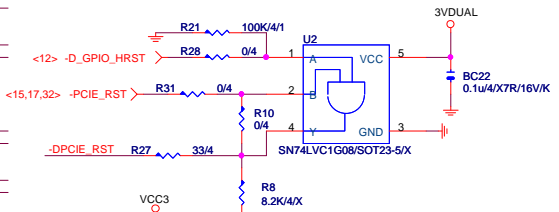
PA EXP TXP0	PAC5	0.22u4/X5R6.3V/K	PA EXP TXP0 C
PA EXP TXN0	PAC4	0.22u4/X5R6.3V/K	PA EXP TXN0 C
PA EXP TXP1	PAC6	0.22u4/X5R6.3V/K	PA EXP TXP1 C
PA EXP TXN1	PAC7	0.22u4/X5R6.3V/K	PA EXP TXN1 C
PA EXP TXP2	PAC8	0.22u4/X5R6.3V/K	PA EXP TXP2 C
PA EXP TXN2	PAC9	0.22u4/X5R6.3V/K	PA EXP TXN2 C
PA EXP TXP3	PAC10	0.22u4/X5R6.3V/K	PA EXP TXP3 C
PA EXP TXN3	PAC11	0.22u4/X5R6.3V/K	PA EXP TXN3 C
PA EXP TXP4	PAC12	0.22u4/X5R6.3V/K	PA EXP TXP4 C
PA EXP TXN4	PAC13	0.22u4/X5R6.3V/K	PA EXP TXN4 C
PA EXP TXP5	PAC14	0.22u4/X5R6.3V/K	PA EXP TXP5 C
PA EXP TXN5	PAC15	0.22u4/X5R6.3V/K	PA EXP TXN5 C
PA EXP TXP6	PAC16	0.22u4/X5R6.3V/K	PA EXP TXP6 C
PA EXP TXN6	PAC17	0.22u4/X5R6.3V/K	PA EXP TXN6 C
PA EXP TXP7	PAC18	0.22u4/X5R6.3V/K	PA EXP TXP7 C
PA EXP TXN7	PAC19	0.22u4/X5R6.3V/K	PA EXP TXN7 C
PA EXP TXP8	PAC20	0.22u4/X5R6.3V/K	PA EXP TXP8 C
PA EXP TXN8	PAC21	0.22u4/X5R6.3V/K	PA EXP TXN8 C
PA EXP TXP9	PAC22	0.22u4/X5R6.3V/K	PA EXP TXP9 C
PA EXP TXN9	PAC23	0.22u4/X5R6.3V/K	PA EXP TXN9 C
PA EXP TXP10	PAC24	0.22u4/X5R6.3V/K	PA EXP TXP10 C
PA EXP TXN10	PAC25	0.22u4/X5R6.3V/K	PA EXP TXN10 C
PA EXP TXP11	PAC26	0.22u4/X5R6.3V/K	PA EXP TXP11 C
PA EXP TXN11	PAC27	0.22u4/X5R6.3V/K	PA EXP TXN11 C
PA EXP TXP12	PAC28	0.22u4/X5R6.3V/K	PA EXP TXP12 C
PA EXP TXN12	PAC29	0.22u4/X5R6.3V/K	PA EXP TXN12 C
PA EXP TXP13	PAC30	0.22u4/X5R6.3V/K	PA EXP TXP13 C
PA EXP TXN13	PAC31	0.22u4/X5R6.3V/K	PA EXP TXN13 C
PA EXP TXP14	PAC32	0.22u4/X5R6.3V/K	PA EXP TXP14 C
PA EXP TXN14	PAC33	0.22u4/X5R6.3V/K	PA EXP TXN14 C
PA EXP TXP15	PAC34	0.22u4/X5R6.3V/K	PA EXP TXP15 C
PA EXP TXN15	PAC35	0.22u4/X5R6.3V/K	PA EXP TXN15 C

PA EXP RXP[0..15] >>> PA\_EXP\_RXP[0..15] <4>  
PA EXP RXN[0..15] >>> PA\_EXP\_RXN[0..15] <4>  
PA EXP TXP[0..15] >>> PA\_EXP\_TXP[0..15] <4>  
PA EXP TXN[0..15] >>> PA\_EXP\_TXN[0..15] <4>

# PCIEX16 SLOT

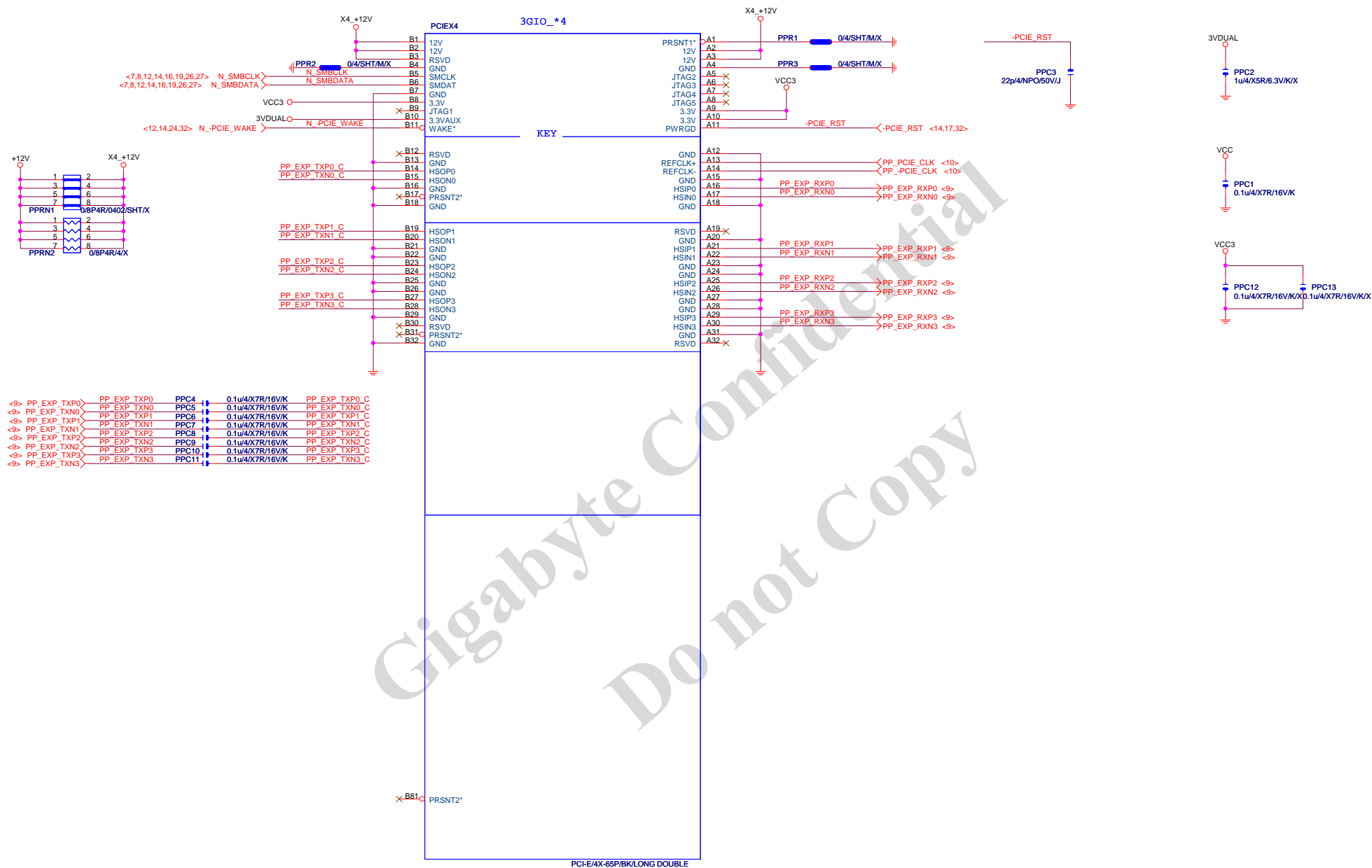


The auxillary reset circuit is only required for PCIe Gen3 margining and functional link training





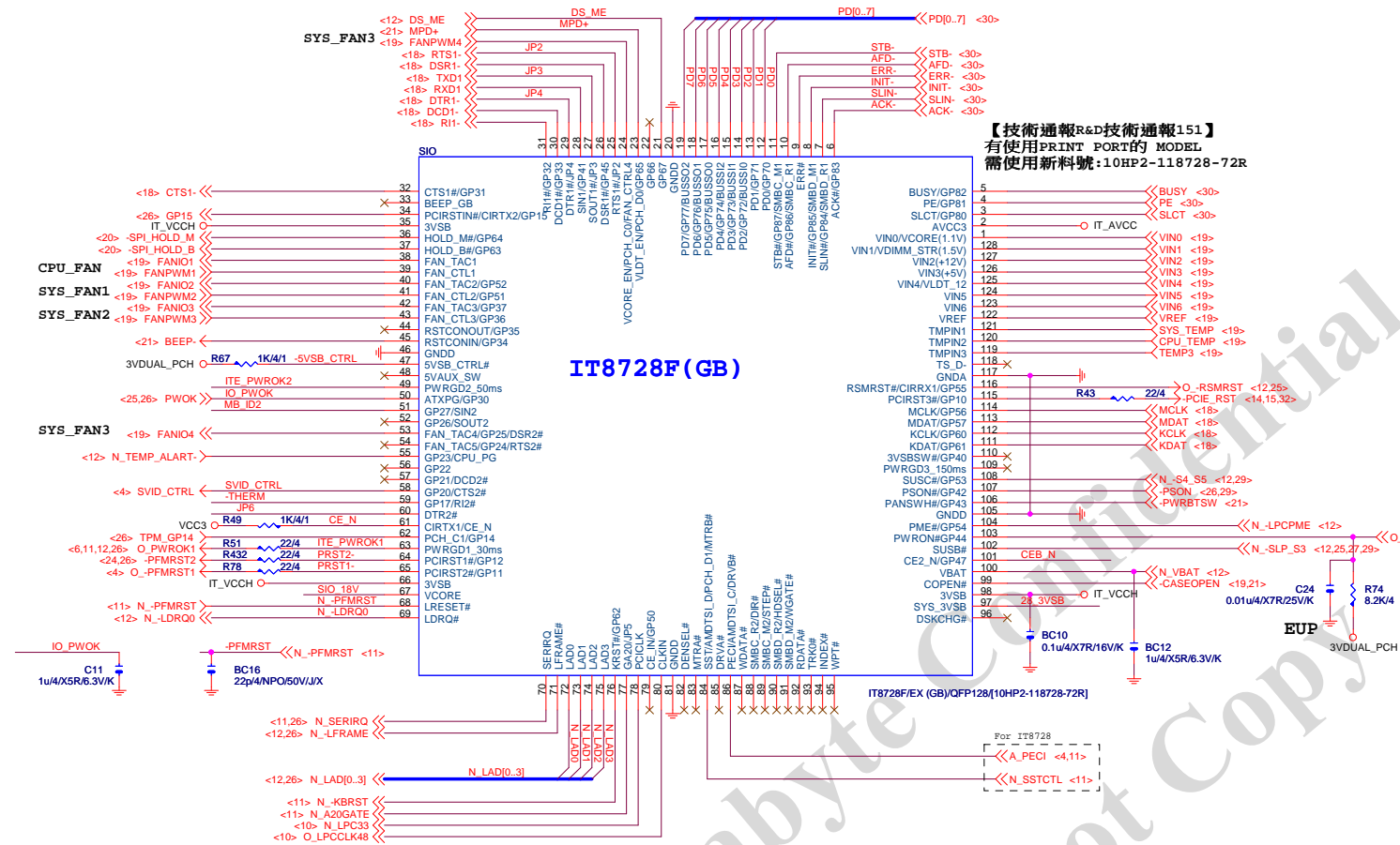
## PCIEX4 SLOT



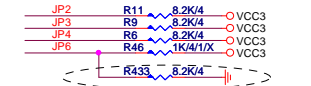
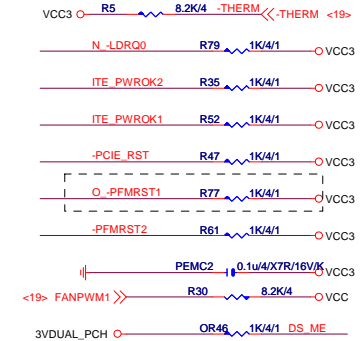
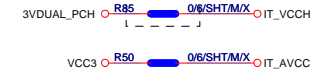
Gigabyte Technology

Title			
PCI EXPRESS X 1 PORT			
Size	Document Number	Rev	
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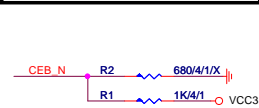
For 8728 EUP function

IT8728-EX  
PULL DOWN ENABLE OVPEUP control by PCH  
3VDUAL\_O 100u4/1 R83 28.3VSBJP3 High SPI-Flash Disable  
Low SPI-Flash Enable

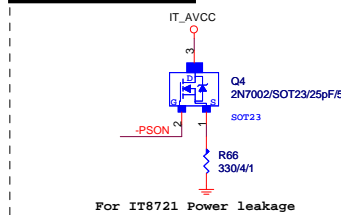
## IT8728F NOTE

	IT8728
PIN121	VCORE_EN/PCH_C0
PIN120	VLDI_EN/PCH_D0
PIN19	ATXPG
PIN31	PCH_C1
PIN53	SST/AMDTSL_D/MTRB#/PCH_D1
PIN55	PECI/AMDTSL_C/DRV#
PIN66	SYS_3VSB
PIN70	GP47
PIN95	VIN2(VCC5)
PIN96	VIN1(VCC12)
PIN97	VIN1/VDIMM_STR(1.5V)
PIN98	VIN0/VCORE(1.1V)/NC

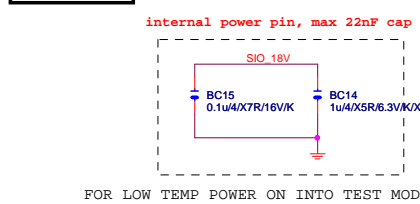
## DUAL BIOS OPT STRAP



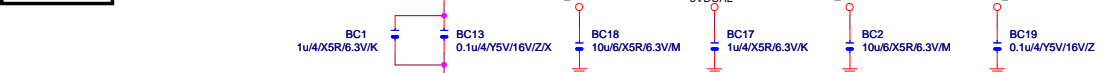
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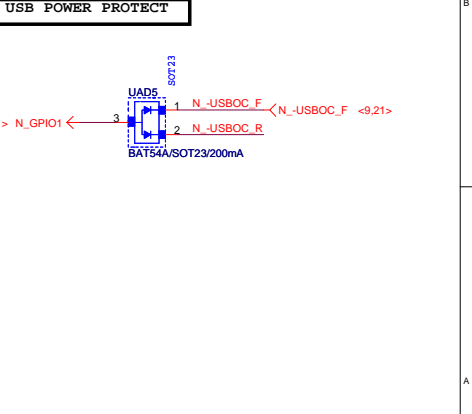
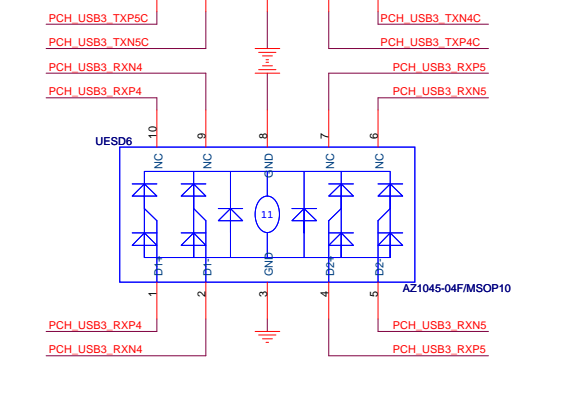
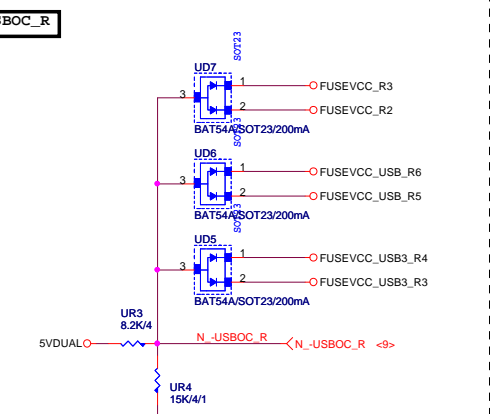
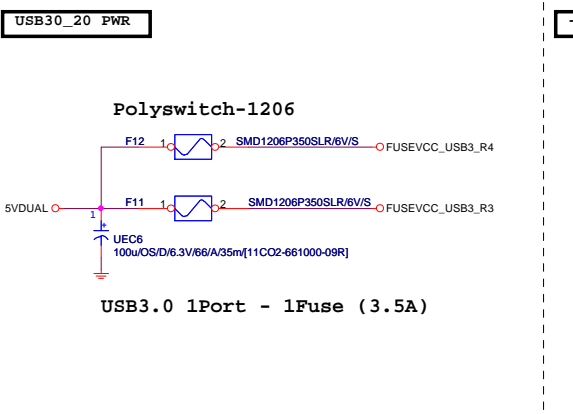
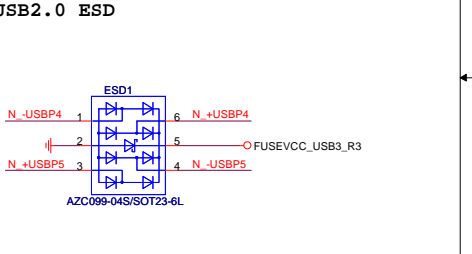
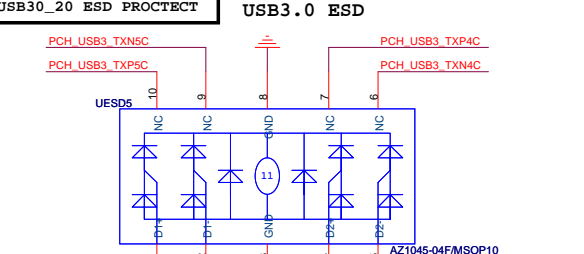
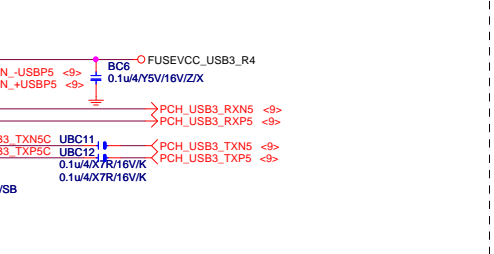
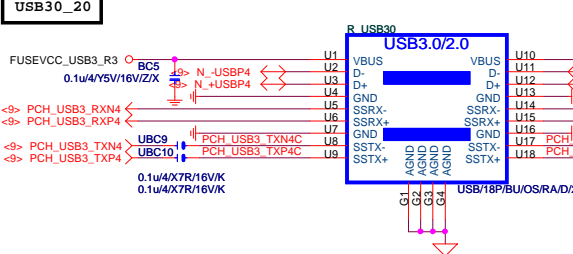
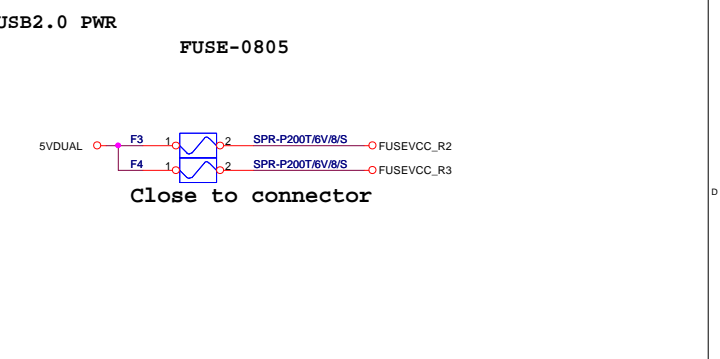
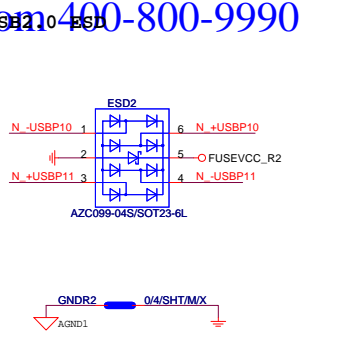
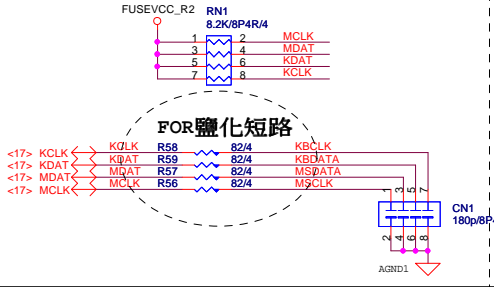
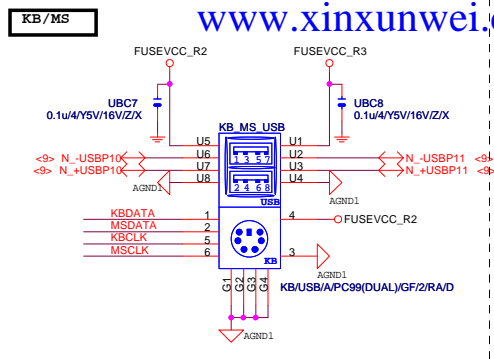
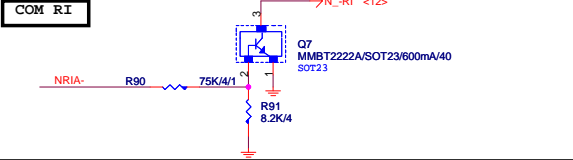
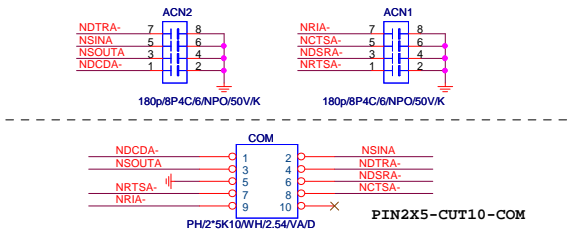
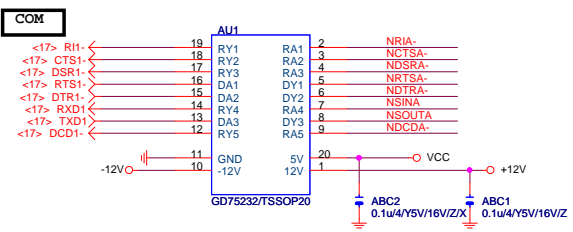


## SIO\_18V



## SIO CAP

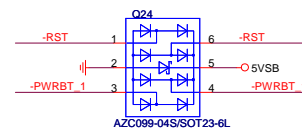
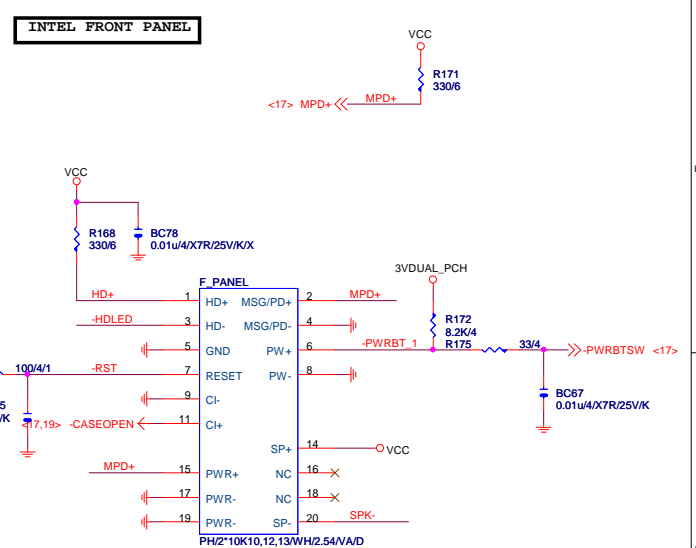
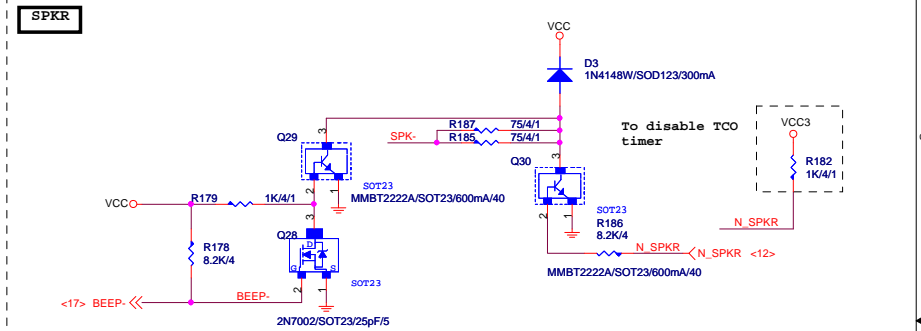
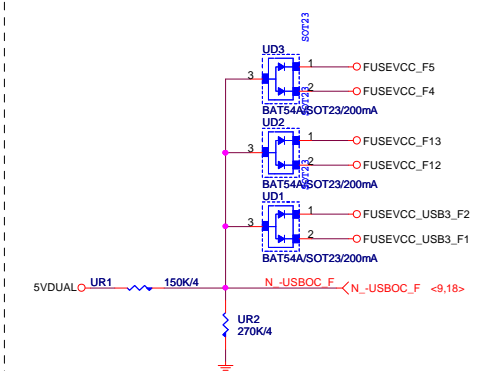












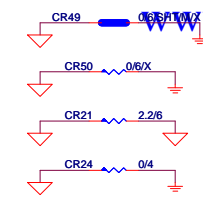
The diagram illustrates a PCB layout for a DAC section, divided into a Digital Area and an Analog Area by a red diagonal line.

**Digital Area:**

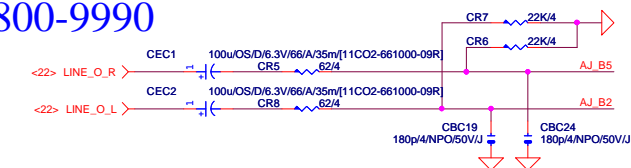
- ICs:** ALC892: X, ALC887: O.
- Capacitors:** CBC34 (10u6/X5R/6.3V/M), CBC35 (10u6/X5R/6.3V/M), CBC32 (22p4/NPO/50V/U/X), CBC38 (0.1u4/X7R/16V/K).
- Resistors:** CR60 (0.4/SMT/M/X), CR61 (22/4), CR65 (0.4/X).
- Traces:** SPdif02\_HDMI, C\_ACZ\_SDOUT, C\_ACZ\_BITCLK, C\_ACZ\_SIN2, C\_ACZ\_SYNC, C\_ACZ\_RST.
- Notes:** CR14/CBC4 close to PCH, 50歐姆:4/5.

**Analog Area:**

- ICs:** VT1708S, ALC892-GR1/QFP48, ALC887.
- Capacitors:** CBC1 (10u6/X5R/6.3V/M), CBC2 (10u6/X5R/6.3V/M), CBC9 (10u6/X5R/6.3V/M), CBC11 (10u6/X5R/6.3V/M), CBC8 (10u6/X5R/6.3V/M), CBC10 (10u6/X5R/6.3V/M), CBC7 (10u6/X5R/6.3V/M), CBC3 (10u6/X5R/6.3V/M).
- Resistors:** CR20 (5.1K/4/1), CR23 (10K/4/1), CR18 (20K/4/1), CR66 (39.2K/4/1), CR44 (47/4/1), CR16 (8.2K/4), CR19 (8.2K/4), CR51 (0/6), CD1 (AZ2225-01L/SOD323/X).
- Traces:** LINE2\_L, LINE2\_R, MIC2\_L, MIC2\_R, LINE1\_L, LINE1\_R, MIC1\_L, MIC1\_R, SENSE\_A, SENSE\_B, SENSE\_C, SENSE\_D, SENSE\_E, SENSE\_F, SENSE\_G, SENSE\_H, SENSE\_I, SENSE\_J, SENSE\_K, SENSE\_L, SENSE\_M, SENSE\_N, SENSE\_O, SENSE\_P, SENSE\_Q, SENSE\_R, SENSE\_S, SENSE\_T, SENSE\_U, SENSE\_V, SENSE\_W, SENSE\_X, SENSE\_Y, SENSE\_Z.
- Notes:** JD resistors close to pin13 of CODEC, JD resistors close to pin34 of CODEC, Can Support Amp Out, 50歐姆:4/10.

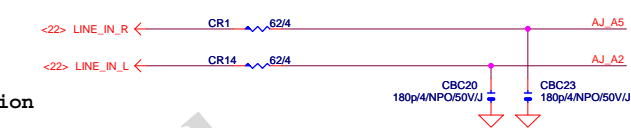


LINE-OUT



Only reserved for ALC888

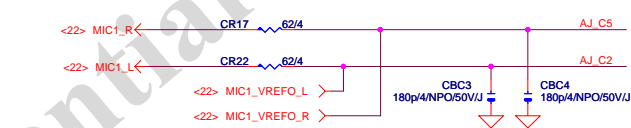
LINE-IN



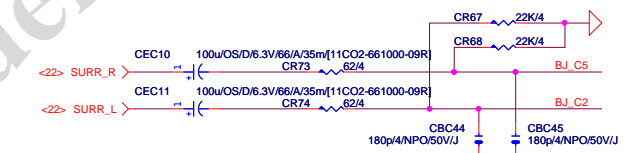
Verify MIC function  
in LINE-in

For 889A/888

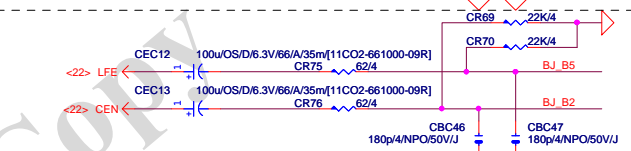
MIC-IN



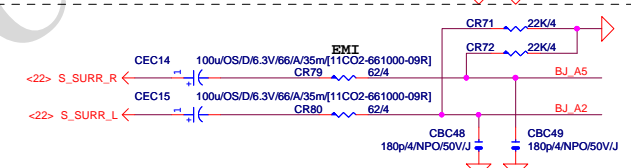
SURROUND



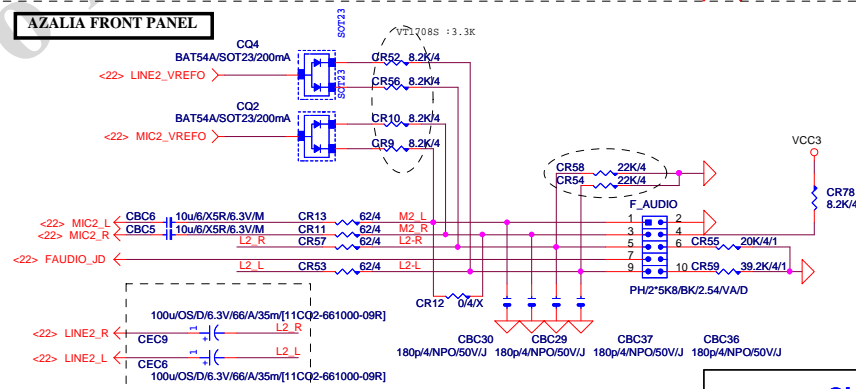
CEN/LFE



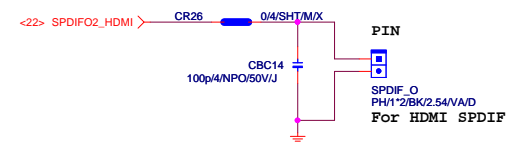
SURR BACK



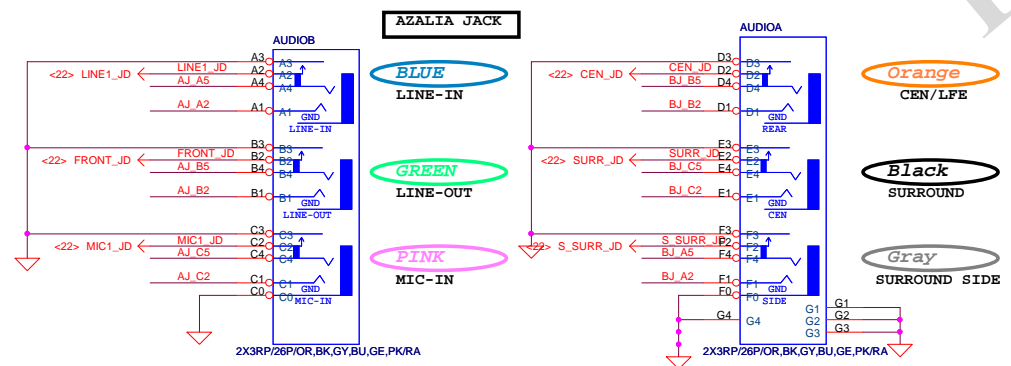
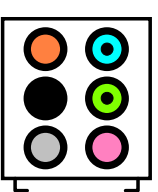
AZALIA FRONT PANEL

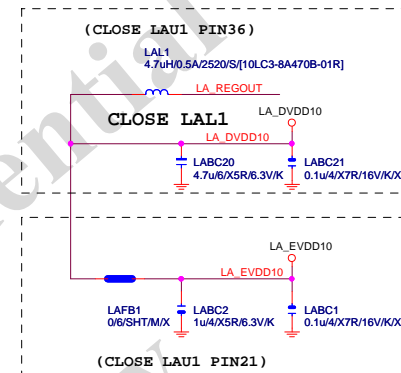
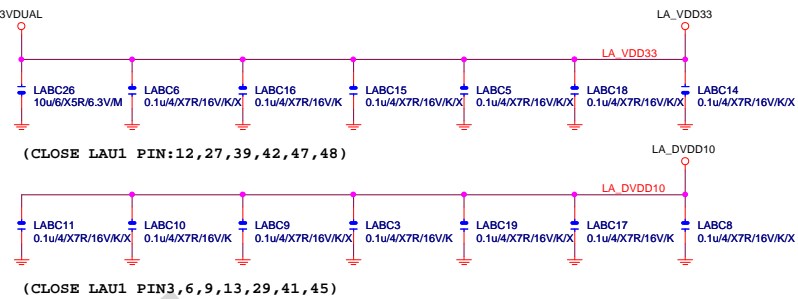


SPDIF\_OUT

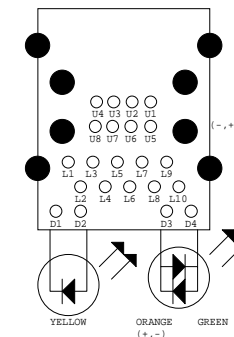


AZALIA JACK

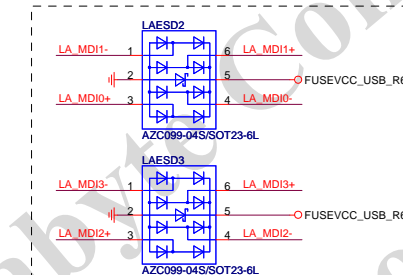




	RTL8111E
AVDD33	3.3V
DVDD33	3.3V
VDDREG	3.3V
DVDD10	1.05V



SBCCLK--&gt;50歐姆:[18/4/10/4/18]



LAESD1

LA\_LED\_D2 1 6 LA\_LED\_LINK1000

2 5 LAN 3VDUAL\_LED

LA\_LED\_ACT\_TXRX 3 4 LA\_LED\_LINK100

AZC099-04S/SOT23-6L

UBESD3

N\_+USBP2 1 6 N\_-USBP2

2 5 FUSEVCC\_USB\_R5

N\_-USBP3 3 4 N\_+USBP3

AZC099-04S/SOT23-6L

LABC22  
0.01u4/X7R/25V/K/X

|| L1 L2 L3 L4 L5 L6 L7 L8 L9 L10

LA MD10+  
LA MD10-  
LA MD11+  
LA MD11-  
LA MD12+  
LA MD12-  
LA MD13+  
LA MD13-

|| L1 L2 L3 L4 L5 L6 L7 L8 L9 L10

LABC25 0/4/SHT/M/X

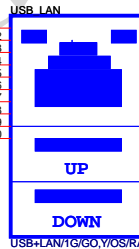
USB LAN

D1 U1  
D2 U1  
D3 U1  
D4 U1  
U1  
U2  
U3  
U4  
U5  
U6  
U7  
U8

UP

DOWN

USB/LAN/IG/GO/Y/OS/RAD/H2/C/ES



1. (紅色/12CORE/三倍):USB+LAN/1G/GO,Y/OS/RA/D/1/RED
2. (黑色/12CORE):USB+LAN/1G/GO,Y/OS/RA/D/1
3. (黑色/8CORE):USB+LAN/1G/GO,Y/OS/RA/D/8C

**FUSE-0805**

F10 1 2 SPR-P2007/6V/8/S FUSEVCC\_USB\_R  
LABC7 0.1uH/4X7R/16V/K

5VDUAL F9 1 2 SPR-P2007/6V/8/S FUSEVCC\_USB\_R  
LABC23 0.1uH/4X7R/16V/K

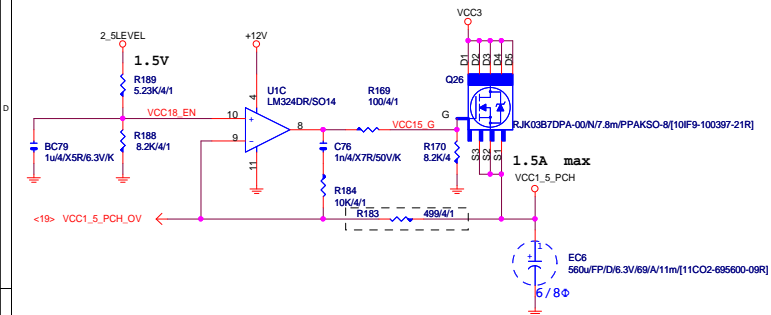
UEC4  
100uS/D/6.3V/66A/35mF [11C02-661000-09R]

LAR24 0/6/SHT/M/X

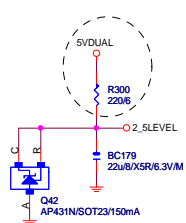
料號	規格	廠商
11NR6-702009-0ER	1G LAN (12core)	UDE
11NR6-702009-91R	1G LAN(8 core)	FOXCONN
11NR6-702009-92R	1G LAN(8 core)	UDE
11NR6-702009-11R	1G LAN(12core/RED)	UDE
11NR6-702009-12R	1G LAN(8 core/RED)	FOXCONN

注意:USB PORT(目前:暫代6,7PORT)  
USB-->90歐姆:[15/4.5/7.5/4.5/15]

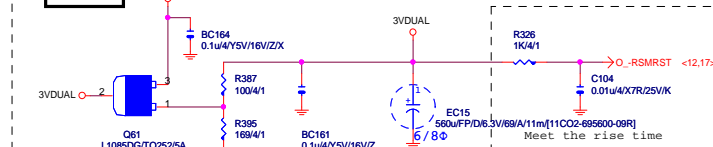
VCC1_8_PCH	
------------	--



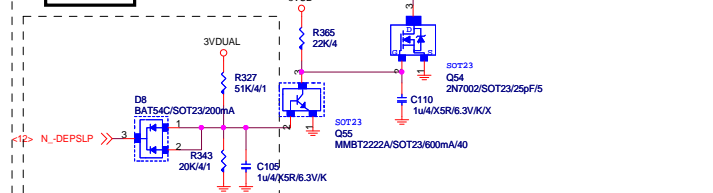
ERP



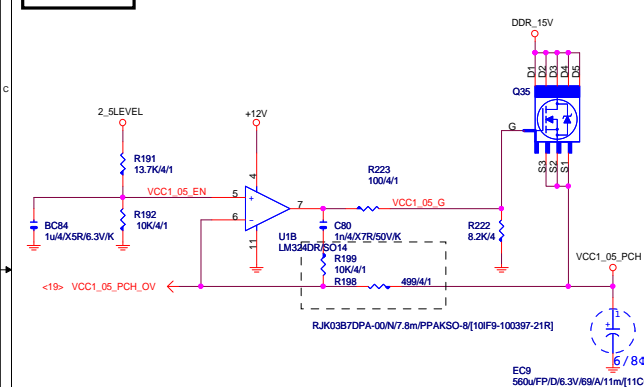
## 3VDUAL



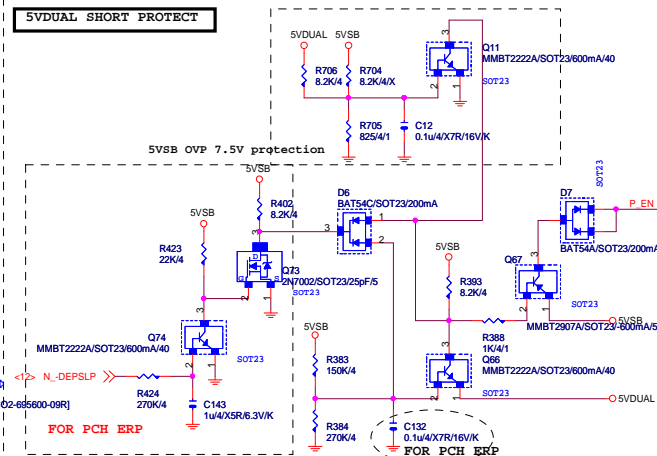
-RSMRST



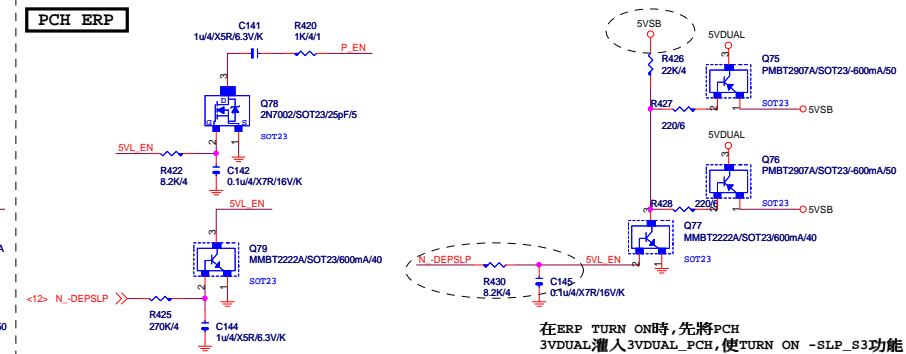
## VCC1\_05\_PCH



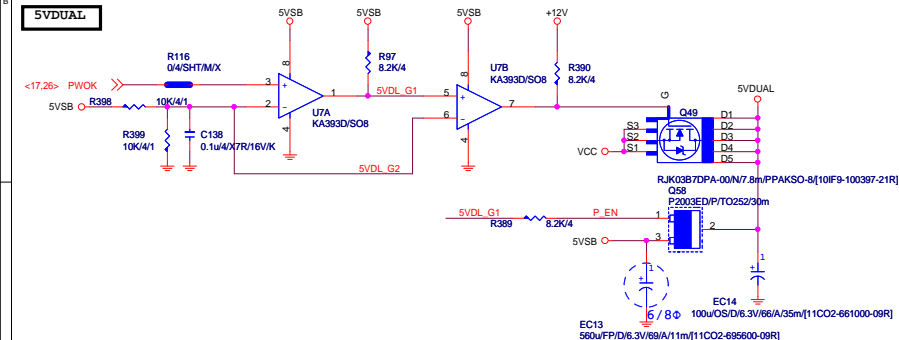
## 5VDUAL SHORT PROTECT



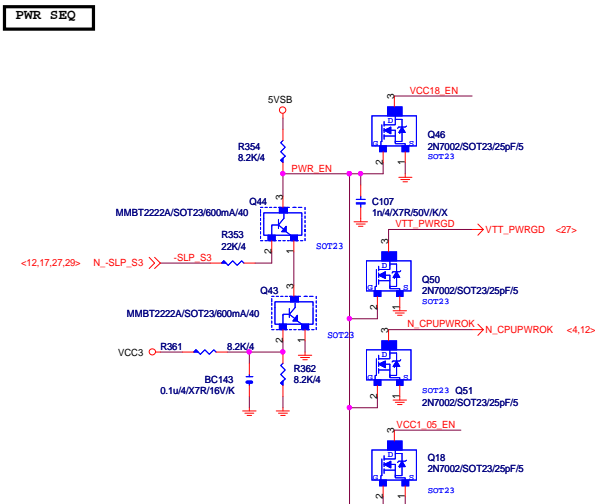
PCH ERP



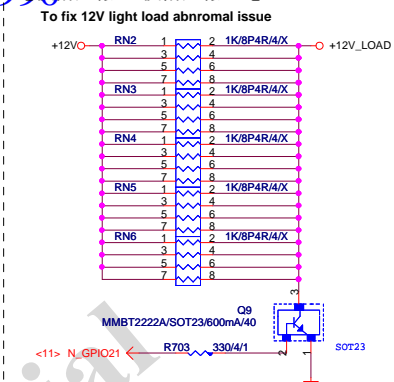
## 5VDUAL



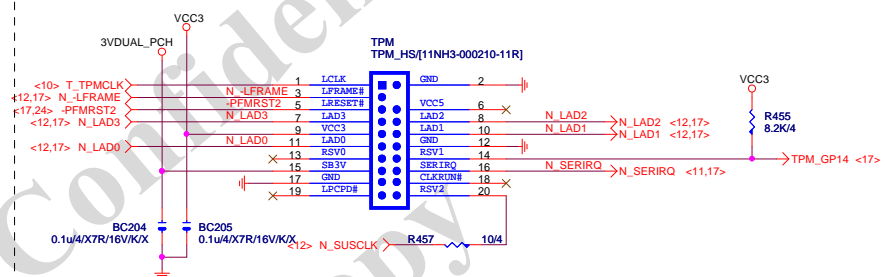
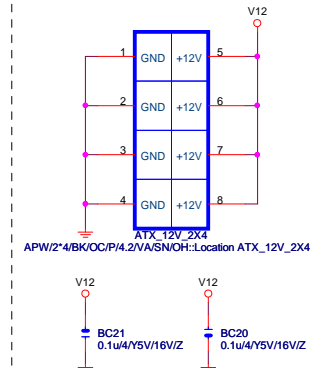
PWR SEQ
---------



【技術通報R&D技術通報153】



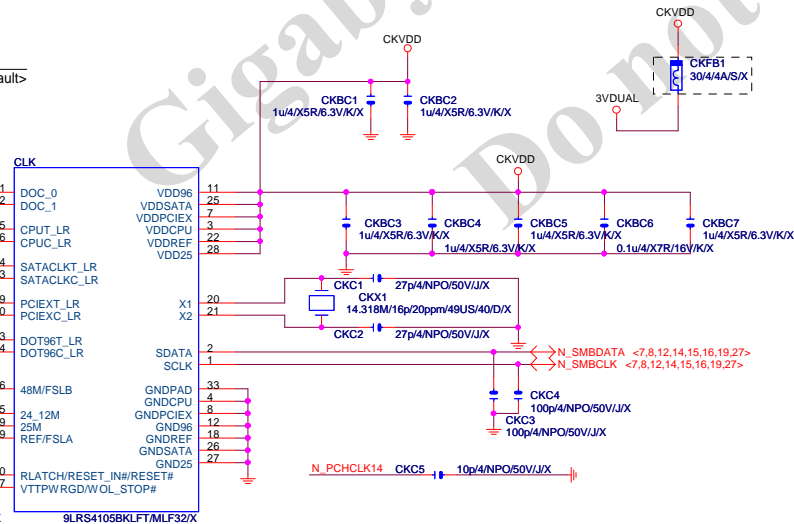
**ATXX4 POWER CONNECTOR**



### CPU Frequency Selection

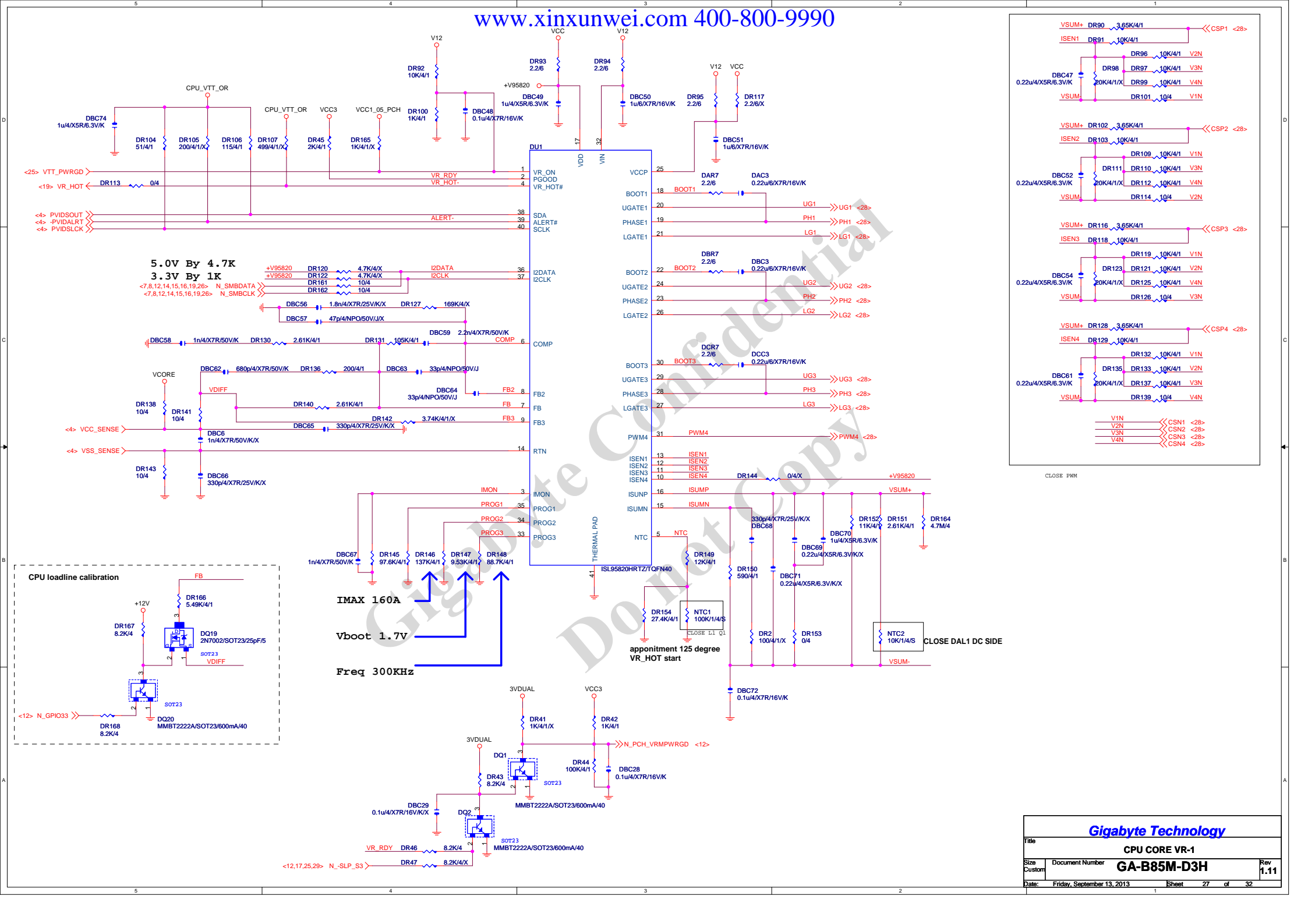
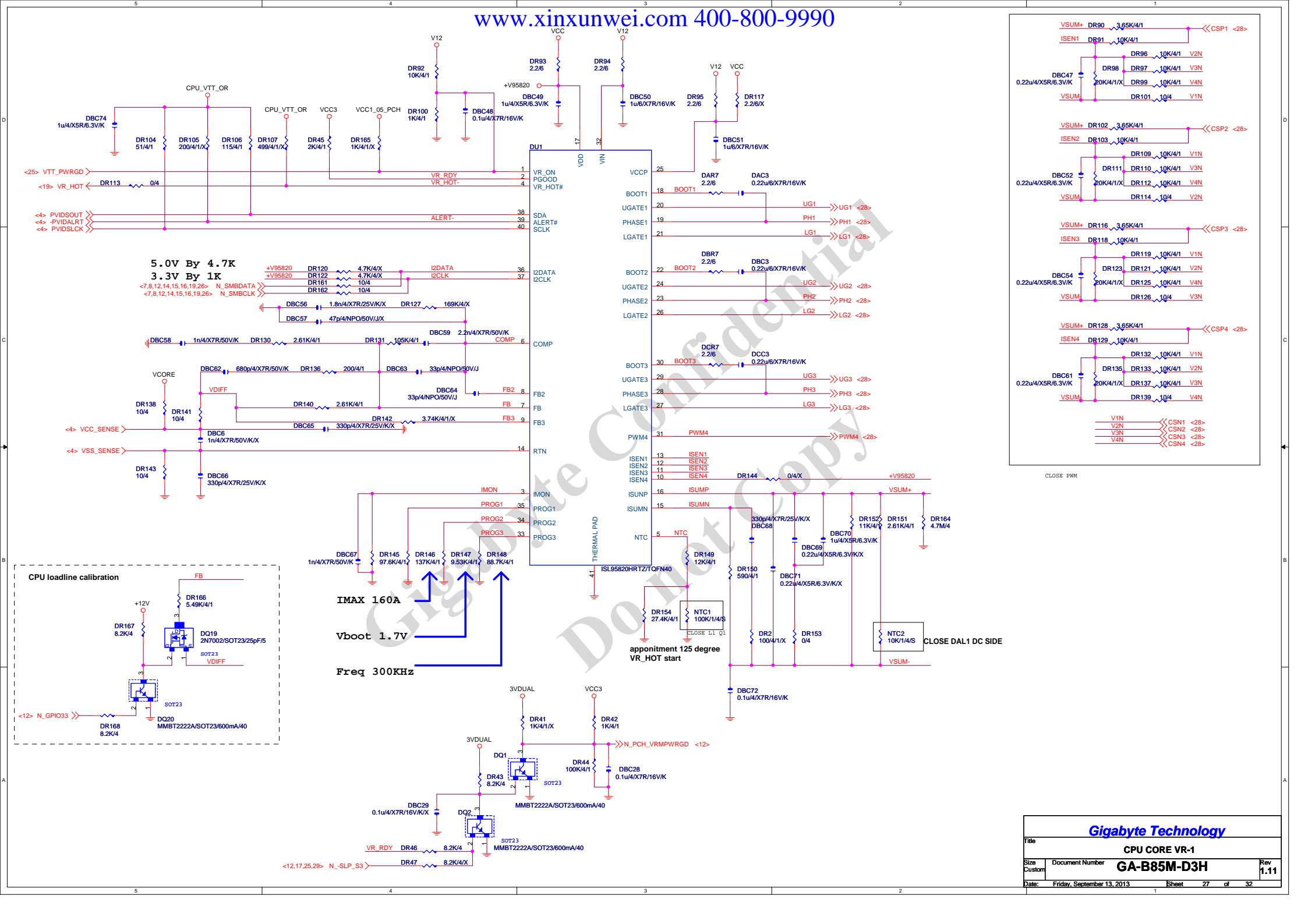
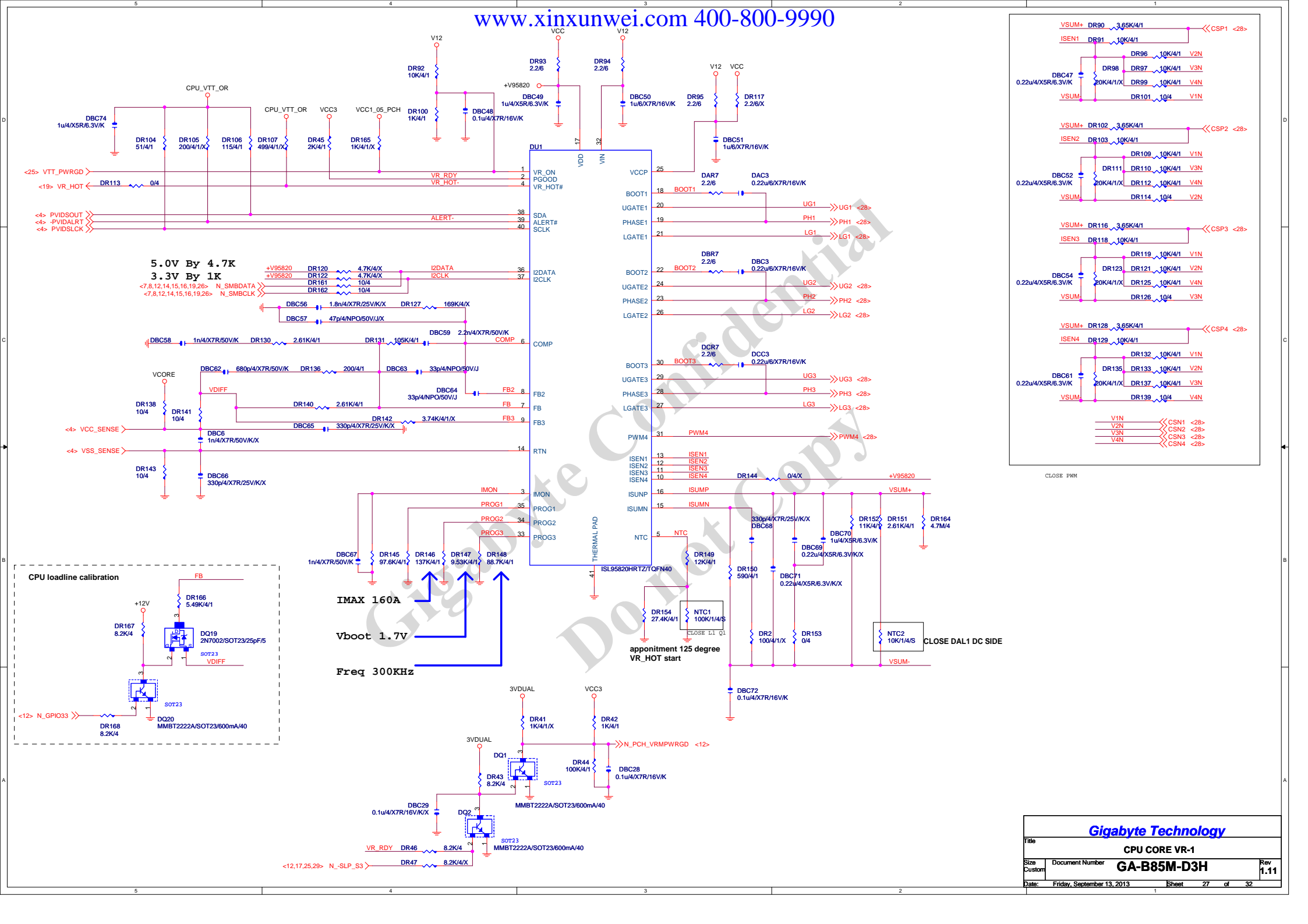
Timing diagram for the LPC4330 showing clock signals CKVDD, CKVDD, and CKVDD. The diagram includes signal names, pin numbers, and timing parameters.

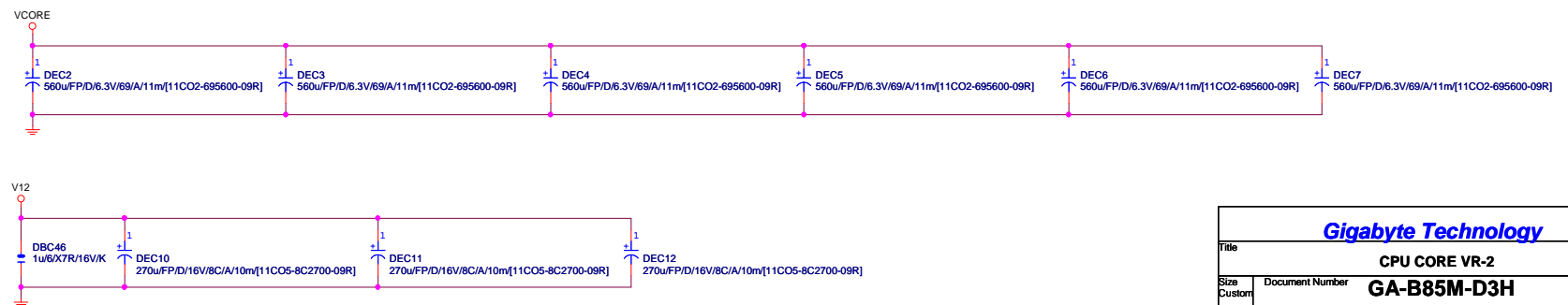
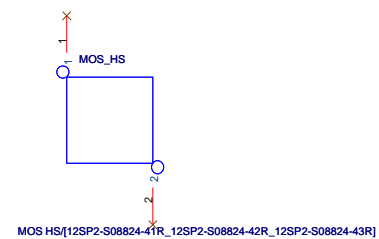
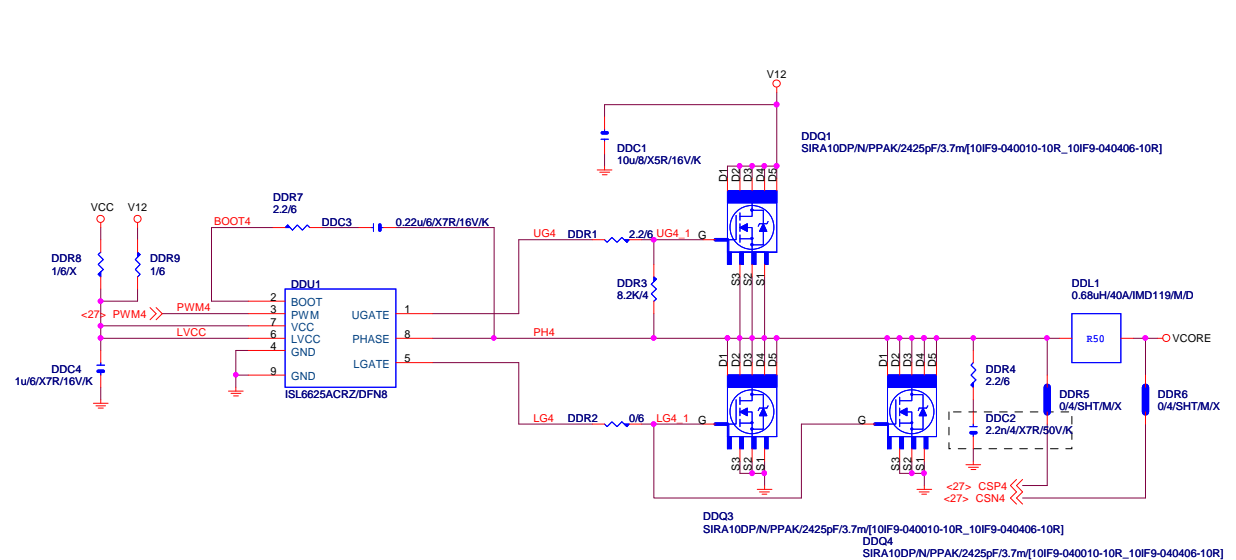
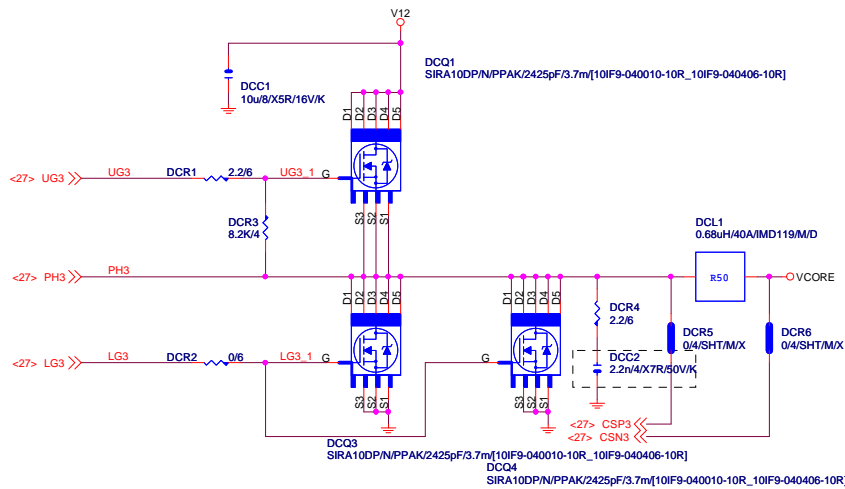
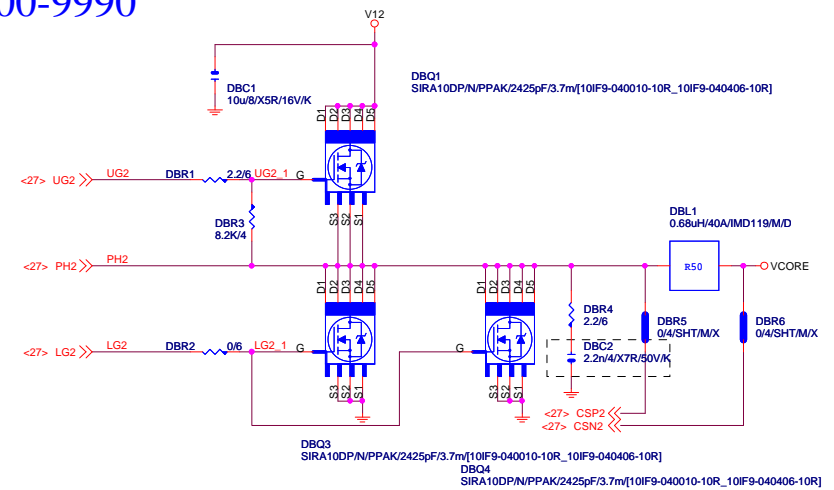
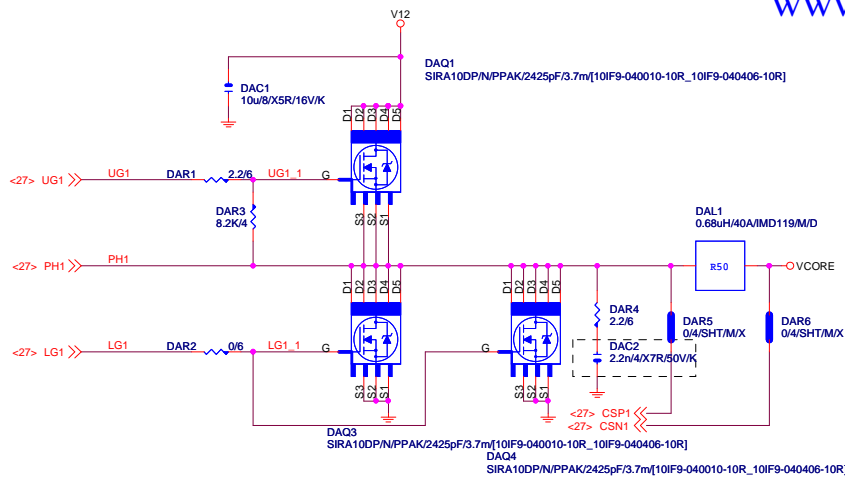
Signal	Pin	Frequency	Duty Cycle
CKVDD	1	200M	166M
CKVDD	2	200M	166M
CKVDD	3	200M	166M



## PWOK PATCH



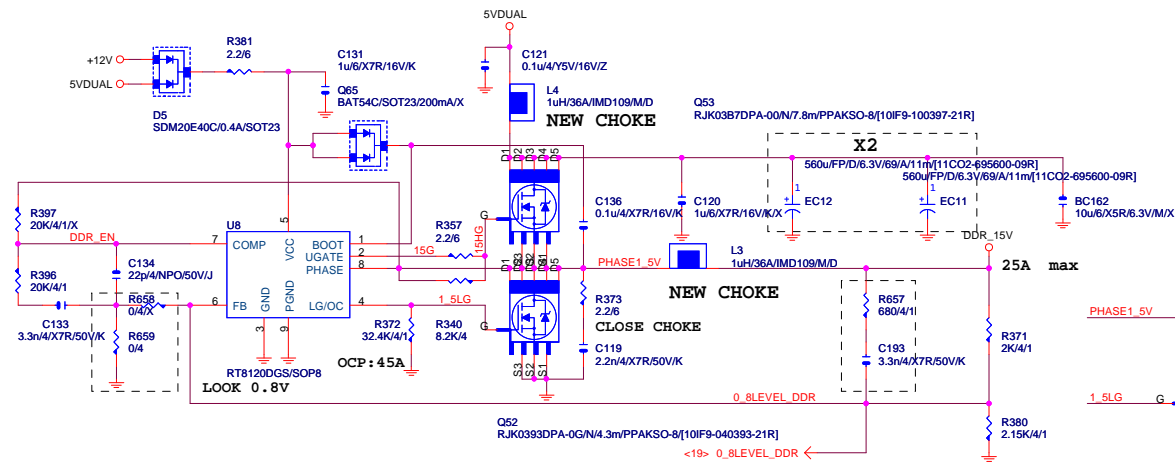




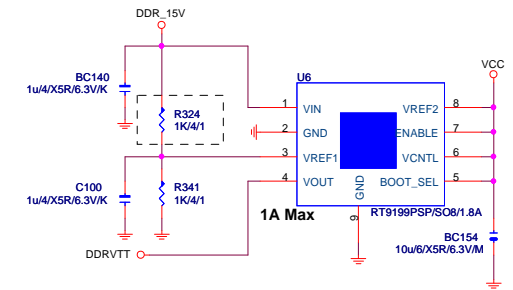
Gigabyte Technology

Title			
CPU CORE VR-2			
Size	Document Number	GA-B85M-D3H	
Custom		Rev 1.11	
Date:	Friday, September 13, 2013	Sheet	28 of 32

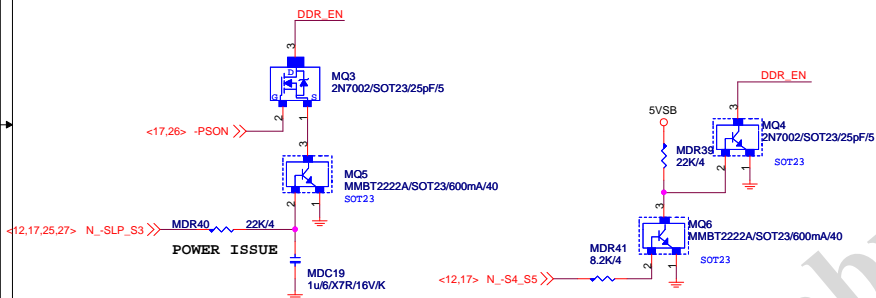
## DDR15V



## DDRVRTT



## PWR SEQ



VIN=5V, VOUT=1.5V, IOUT=25A, PHASE=1

IRMS=11.45A

560u/FP/D/6.3V/68/8m RIPPLE CURRENT=4.7A

Coefficient=1.7(85°C), 1(105°C)

VIN Ripple current=4.7X1.7=7.99A(85°C)

-->故固態電容須2X7.99=15.98>11.45A

Rocset=(Iocp\*Lgate,rdson)/Iocset

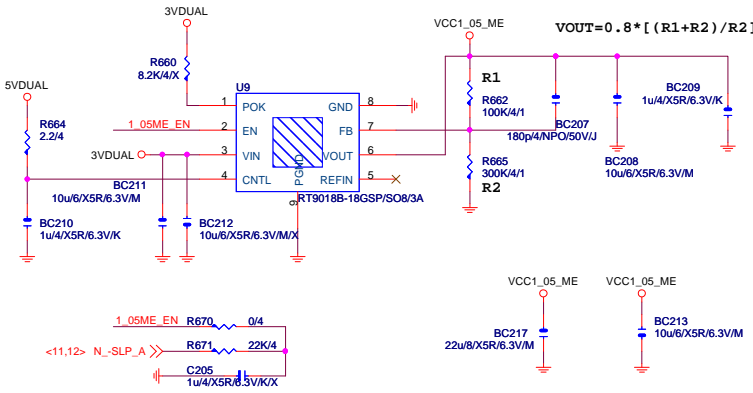
Rocset=(45A\*6.7mOhm)/10uA = 30K

Iocset=10uA

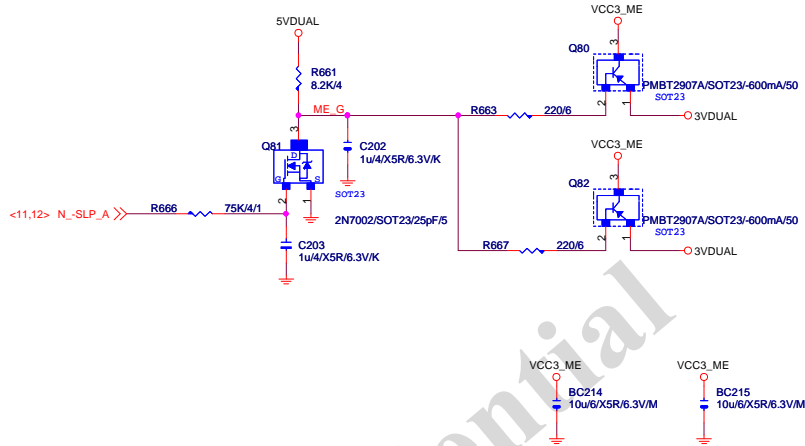
**Gigabyte Technology**

Title			
DDR POWER			
Size	Document Number	GA-B85M-D3H	
Custom			Rev 1.11
Date:	Friday, September 13, 2013	Sheet	29 of 32

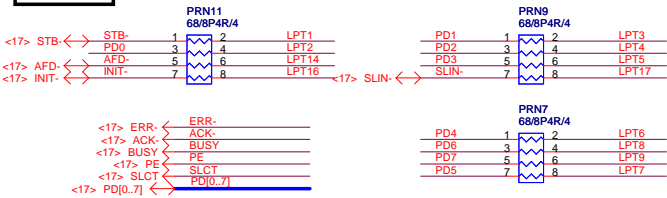
# VCC1\_05\_ME



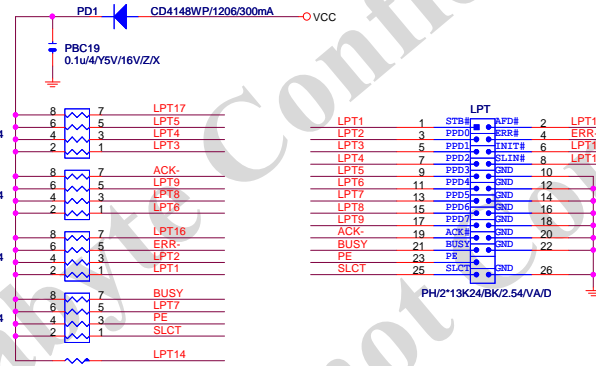
www.xinxiunwei.com 400-800-9990



# LPT PORT

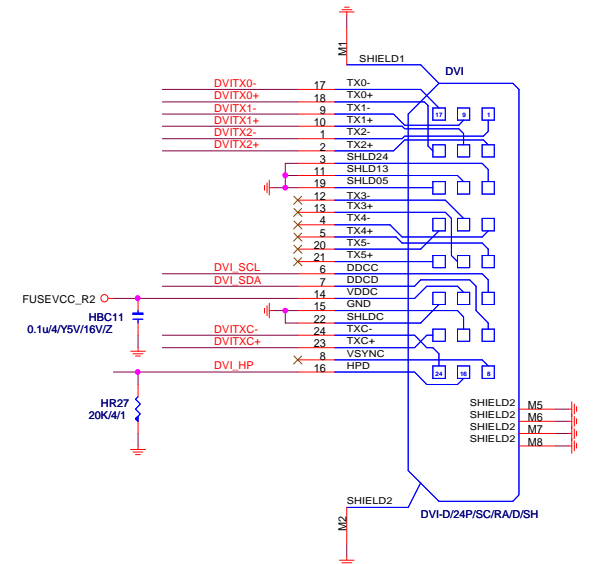
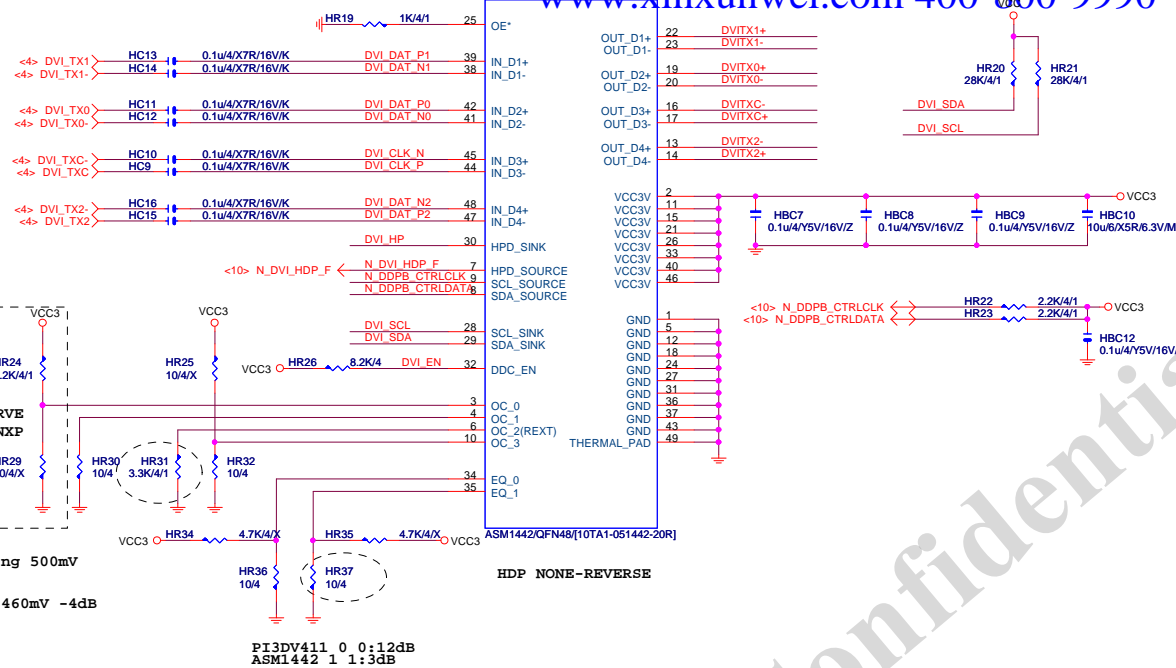


【技術通報R&D技術通報151】  
33ohm Change to 68ohm

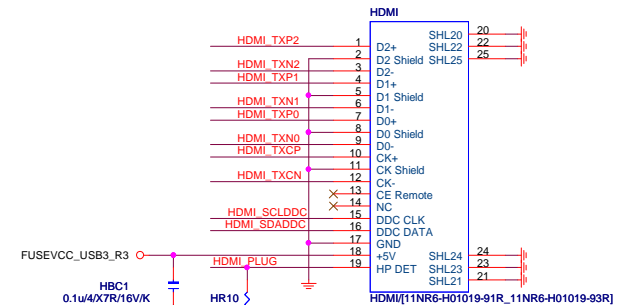
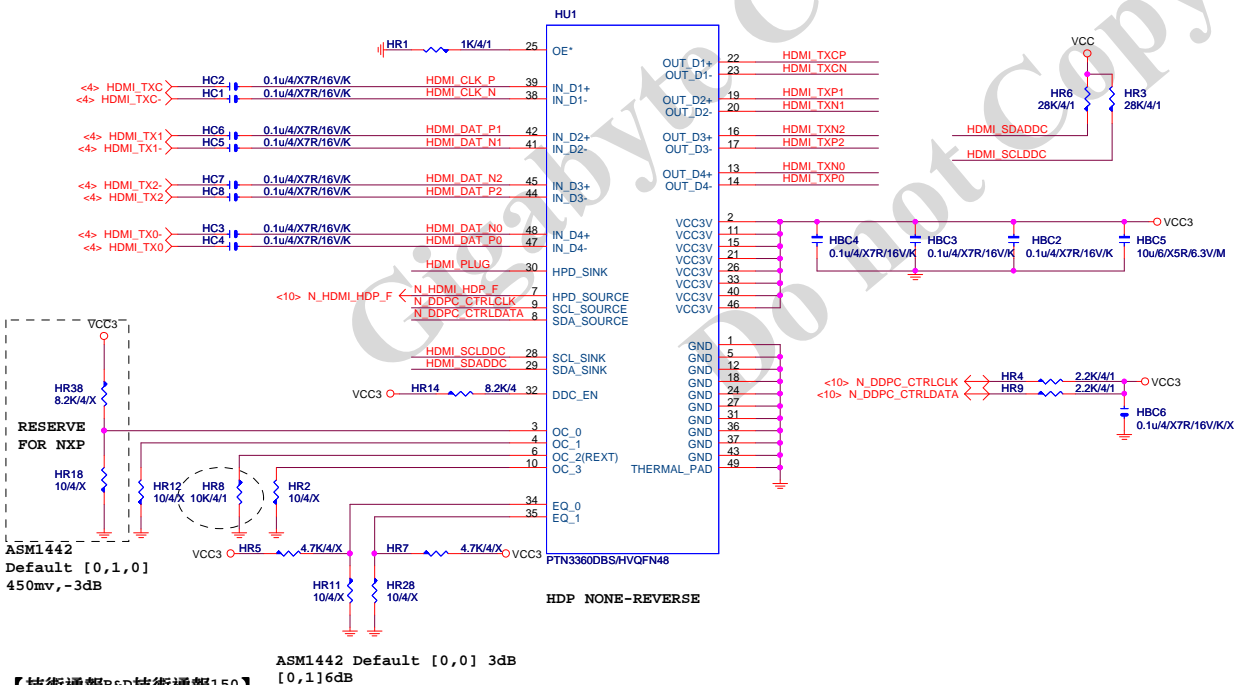


LPT			
LPT1	1	STB#	2
LPT2	3	PD0	4
LPT3	5	PD1	6
LPT4	7	PD2	8
LPT5	9	PD3	10
LPT6	11	PD4	12
LPT7	13	PD5	14
LPT8	15	PD6	16
LPT9	17	PD7	18
ACK	19	PD8	20
BUSY	21	PD9	22
PE	23	PD10	24
SLCT	25	PD11	26

## DVI LEVEL SHIFT



## HDMI LEVEL SHIFT



【技術通報R&amp;D技術通報150】

HDMI eye diagram 1.4版(deep color)會fail

原因: 因目前的HDMI訊號過長,造成RISING TIME過慢,而會壓到eye diagram

改善: ASMEDIA ASM1442 : 3.16K(PIN6 PULL DOWN電阻) 10ohm(PIN4 PULL DOWN電阻)

Gigabyte Technology

Title		
DVI		
GA-B85M-D3H		
Size Custom	Document Number	Rev 1.11
Date: Friday, September 13, 2013	Sheet 31	of 32

Title			
ITE IT8892E			
Size	Document Number		Rev
Custom	GA-B85M-D3H		1.1
Date:	Friday, September 13, 2013	Sheet	32 of 32